Interference Search

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L17	1	((first adj clock\$4) same (second adj clock) same (enabl\$4 or read\$4) adj clock same register\$4 same location).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 12:47
L18	0	((first adj clock\$4) same (second adj clock) same (enabl\$4 or read\$4) adj clock same register\$4 same location same synchron\$8). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 12:48
L19	7	((first adj clock\$4) same (second adj clock) same (enabl\$4 or read\$4) adj clock same (intermediate or register\$4 or buffer\$4 or location or target or source) same synchron\$8).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 12:52
L20	1	((first adj clock\$4) same (second adj clock) same (enabl\$4 or read\$4) adj clock same (intermediate or target or source) with (buffer\$4 or memory or register\$4 or location\$4) same synchron\$8).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 12:52
L21	9	((first adj clock\$4) same (second adj clock) same (enabl\$4 or read\$4) adj clock same (intermediate or target or source or first with second with third or first or second or third) with (buffer\$4 or memory or register\$4 or location\$4) same synchron\$8). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 12:53

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurais	Time Stamp
12	155	(375/356,358,372,354,370).ccls. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 10:43
ß	124	(375/356,358,372,354,370).ccls. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data and @ad<="20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 10:43
L4	84	(375/356,358,372,354,370).cds. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data and @ad< = "20020102" and (multiple or plurality or target or source or first or second) near4 (memor\$4 or buffer\$4 or register\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 10:44
L5	80	(375/356,358,372,354,370).cds. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data and @ad <= 20020102* and (multiple or plurality or target or source or first or second) near4 (memor\$4 or buffer\$4 or register\$4) and synchron\$8 with clock\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 11:18
L6	52	(375/356,358,372,354,370).cds. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data and @ad < ■20021012* and (multiple or plurality or target or source or first or second) near4 (memor\$4 or buffer\$4 or register\$4) and synchron\$8 with second adj4 clock\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 10:45

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L12	44	(710/3,5,33,110,310).ccis. and first adj4 clock and second adj4 clock and second adj4 clock and (enable or read;44 or third) adj4 clock and (m2020102° and (multiple or plurality or target or source or first or second) near4 (memor\$4 or buffer\$4 or register\$44) and synchron\$8 with clock\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 12:00
L13	16	(375/356,358,372,354,370),ccls. and first adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and transfer\$4 with data and @ad < = 20020102° and (multiple or plurality or target or source or first with second) near\$4 (memor\$4 or buffer\$4 or register\$4) and (third or intermediate) near\$4 (memor\$4 or buffer\$4 or register\$4) and synchron\$8 with clock\$4	US-PGPUB; USPAT; USOCR; EPO; IPO	OR	OFF	2006/09/26 11:43
L14	14	(710/3,5,33,110,310).ccls, and first adj4 clock and second adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and (moultaple or plurality or target with data and @ad < ≈ 2002102° and (multiple or plurality or target or source or first or second) near4 (memor\$4 or buffer\$4 or register\$4) and synchron\$8 with second adj clock\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 12:01
L15	52	(713/400).ccls. and first adj4 clock and second adj4 clock and (enable or readj4 or bitrid) adj4 clock and transfer;4 with data and (maltiple or purality or target or source or first or second) near4 (memors4 or buffer;4 or register;4) and synchron;58 with second adj clock;4	US-PGPUB; USPAT; USOCR; EPO; IPO	OR	OFF	2006/09/26 12:43

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ט	52	(375/356,358,372,354,370).cds. and first adj4 clock and second adj4 clock and (enable or read}4 or third) adj4 clock and transfers4 with data and @ad< = "20020102" and (multiple or plurality or target or source or first with second or third or intermediate) near4 (memors4 or buffers4 or registers4) and synchron\$8 with clock\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 11:21
L8	3	(370/235,230,298,301).cds, and first adj4 clock and second adj4 clock and second adj4 clock and (enable or read\$4 or third) adj4 clock and (and €a4 <= 20020102° and (multiple or plurality or target or source or first or second) near4 (memor\$4 or buffer\$4 or register\$4) and synchron\$8 with clock\$4	US-PGPUB; USPAT; USOCR; EPO; IPO	OR	OFF	2006/09/26 11:19
L9	0	(455/502).ccls. and first adj4 clock and second adj4 clock and (enable or read94 or third) adj4 clock and transfer\$4 with data and @ad = "20020.102" and (multiple or plurafity or target or source or first or second) near4 (memor\$4 or buffer\$4 or register\$4) and synchron\$8 with clock\$4		OR	OFF	2006/09/26 11:19
L10	98	(365/189.01,189.12,189.04,220). cds. and first adj4 clock and second adj4 clock and cenable or readş4 or third) adj4 clock and transfer‡4 with data and @ad = "20020102" and (multiple or plurality or target or source or first or second) near4 (memor\$4 or buffer\$4 or register\$4) and synchron\$8 with clock\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 11:20
L11	111	(713/400).ccts. and first adj4 clock and second adj4 clock and (enable or readj4 or third) adj4 clock and transferş4 with data and @ad = "20020102" and (multiple or plurality or target or source or first or second) near4 (memorş4 or bufferş4) and synchronş8 with clockş4		OR	OFF	2006/09/26 12:43

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L16	8	(713/400).cds. and first adj4 clock and second adj4 clock and (enable or readj4 or third) adj4 clock and transfers4 with data and (enable enable enable). The control of th		OR	OFF	2006/09/26 12:46
L17	1	((first adj clock\$4) same (second adj clock) same (enabl\$4 or read\$4) adj clock same register\$4 same location).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF '	2006/09/26 12:47
L18	0	((first adj clock\$4) same (second adj clock) same (enabl\$4 or read\$4) adj clock same register\$4 same location same synchron\$8). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 12:48
L19	7	((first adj clock\$4) same (second adj clock) same (enabl\$4 or read\$4) adj clock same (intermediate or register\$4 or buffer\$4 or location or target or source) same synchron\$8).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 12:52
L20	1	((first adj clock\$4) same (second adj clock) same (enabl\$4 or read\$4) adj clock same (intermediate or target or source) with (buffer\$4 or memory or register\$4 or location\$4) same synchron\$9).clm.	US-PGPUB; USPAT; USOCR; EPO; IPO	OR	OFF	2006/09/26 12:52
L21	9	((first adj clock\$4) same (second adj clock) same (canbl\$4 or read\$4) adj clock same (intermediate or target or source or first with second with third or first or second or third) with (buffer\$4 or memory or register\$4 or location\$4) same synchron\$8).ctm.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 12:53
S1	5595	elastic with buffer\$4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/23 11:56
S2	3703	elastic with buffer\$4 and @ad < ="20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/23 11:57

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S3	707	elastic with buffer\$4 and register\$4 and @ad<="20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR °	OFF	2006/09/24 13:18
54	125	elastic with buffer\$4 and register\$4 and first adj4 clock and second adj4 clock and @ad<="20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/23 12:08
S5	2	elastic with buffer\$4 same register\$4 same first adj4 clock same second adj4 clock and @ad<="20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/23 11:58
56	88	elastic with buffer\$4 and (plurality or multiple or intermediate or first with second with third) with register\$4 and first adj4 clock and second adj4 clock and @ad<="20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/23 12:12
S7	75	elastic with buffer\$4 and (plurality or multiple or intermediate or first with second with third) with the register\$4 and first adj\$4 clock and eached or ready or start or detect\$4) with (writs\$4 or transfer\$4 or mov\$4) and @ad <= "20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/23 13:19
S8	86	elastic with buffer\$4 and (plurality or multiple or intermediate or first with second with third) with the rejiter\$4 and first adj\$4 clock and eacond adj\$4 clock and (enable or ready or start or detect\$4) with (writs\$4 or bransfer\$4 or mox\$4 or signal) and @ad < = 20020102*	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/23 12:13
59	87	elastic with buffer\$4 and (plurality or multiple or intermediate or first with second with third) with register\$4 and first adj4 clock and second adj4 clock and (enable or control or ready or start or detect\$4) with (writ\$4 or transfer\$4 or mov\$4 or signal) and @ad <= "20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/23 12:13

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T	70	elastic with buffer\$4 and (plurality	US-PGPUB:	OR	OFF	2006/09/23 13:19
S10	70	elastic with ourseys and putrateys or multiple or intermediate or first with second with third) with register\$4 and first adj\$6 clock and second adj\$6 clock and (enable or ready or start or detect\$4) with (writ\$4 or transfer\$4 or mov\$4) and synchroni\$4 with clock\$4 and @ads < "20020102"	USPAT; USOCR; EPO; JPO	GR .	OFF	2000/09/23 13:19
511	72	elastic with buffer\$4 and (plurality or multiple or intermediate or first with second with third) with register\$4 and first adj4 clock and second adj4 clock and (enable or ready or start or detect\$4) with (writ\$4 or transfer\$4 or mov\$4) and synchroni\$8 with clock\$4 and @ad < ="20020102"	US-PGPUB; USPAT; USOCR; EPO; IPO	OR	OFF	2006/09/23 13:20
S12	470	synchron\$8 same second adj4 clock same (enable or writ\$4 or read\$4) adj4 clock and @ad<="20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/24 13:19
513	327	(first with second or intermediate or middle or extra or several or plurality or multiple) with (registers?4 or buffers?4 or memory) and synchron\$8 same second ad/4 clock same (enable or writs?4 or reads?4) ad/4 clock and (@ad < = "20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/24 13:22
514	187	(first with second or intermediate or middle or extra or several or plurality or multiple) add/ (registers/4 or buffers/4 or memory) and synchrons/8 same second add/4 clock same (enable or writs/4 or reads/4) add/4 clock and @ad < = "20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/24 13:23
S15	26	(first with second or intermediate or middle or extra or several or plurality or multiple) add/4 (register\$4 or buffer\$4 or memory) and synchron\$8 same second adj clock same (enable or writ\$4 or read\$4) adj clock and @ad < "20020102"	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2006/09/26 10:41

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